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CONFIRMATION NO. 1672

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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a DIV of 09/515,875 02/29/2000 PAT 6,287,953  
 which is a CIP of 09/119,934 07/21/1998 PAT 6,146,954

*TN*  
 \*\* FOREIGN APPLICATIONS \*\*\*\*\* *Nme*

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 03/27/2003

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged <i>Examiner's Signature</i> <i>T.N.</i> Initials	CA	5	20	3

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## TITLE

Minimizing transistor size in integrated circuits